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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application of

Applicants : Paul D. Shirley
Serial No. : 10/773,968
Filed : February 6, 2004
Title : **DEVICE AND METHOD FOR FORMING IMPROVED RESIST LAYER**
Docket : MIO 0112 PA/40509.272/00-0265

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

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John D. Reed

Reg. No. 46,506

Sir:

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UNDER 37 CFR §§ 1.56, 1.97, AND 1.98

Applicant submits herewith patents, publications, and other information of which he is aware, which he believes may be material, as defined in 37 CFR §1.56(b), to the examination of this application and in respect of which there may be a duty to disclose in accordance with 37 CFR §1.56(a). While the information referred to in this Information Disclosure Statement may be material pursuant to 37 CFR §1.56(b), the filing of this Information Disclosure Statement is not intended to, pursuant to 37 CFR §1.97(h), constitute an admission that any patent, publication, or other information referred to is, or is considered to be, material to the patentability of this invention. No representation is made that a reference is "prior art" within the meaning of 35 U.S.C. §§ 102 and 103, and Applicant reserves the right, pursuant to 37 C.F.R. § 1.131 or otherwise, to establish otherwise. Further, pursuant to 37 CFR §1.97(g), the filing of this Statement should not be construed as a statement that a search has been made or that no other material information exists.


This Information Disclosure Statement is being filed within the period set forth in 37 CFR §1.97(b) because it is believed to be filed before the mailing date of a first office action on the merits.

Serial No. 10/773,968

Docket No. MIO 0112 PA/40509.272/00-0265

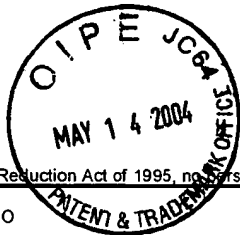
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Respectfully submitted,
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PTO/SB/08B (08-03)

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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

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Complete if Known

Application Number	10/773,968
Filing Date	February 6, 2004
First Named Inventor	Paul D. Shirley
Art Unit	
Examiner Name	
Attorney Docket Number	MIO 0112 PA/40509.272/00-0265

Sheet 2 of 2

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		MICHAEL QUIRK AND JULIAN SERDA, Semiconductor Manufacturing Technology, November 11, 2003, Pages 357 - 360 Chapter 13, Prentice Hall, Upper Saddle River, New Jersey and Columbus, Ohio	

Examiner Signature		Date Considered	
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